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D/Chang
7/25/02

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Examiner Daniel D. Chang at Fax No.: 1-703-872-9318

On 7.23.02

TOWNSEND and TOWNSEND and CREW LLP

By: D. Srinivas Reddy

PATENT
Attorney Docket No.: 015114-045720US
Client Ref. No.: A211 D1 C1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Srinivas Reddy et al.

Application No.: 09/832,685

Filed: April 10, 2001

For: TRISTATE STRUCTURES FOR
PROGRAMMABLE LOGIC DEVICES

Examiner: D. Chang

Art Unit: 2819

AMENDMENT

FAX COPY RECEIVED

JUL 23 2002

Assistant Commissioner for Patents
Washington, D.C. 20231

TECHNOLOGY CENTER 2800

Sir:

In response to the office action mailed January 24, 2002, please amend the
above-identified application as follows:

IN THE CLAIMS:

Please amend claims 33, 48, and 52 as indicated below. Please cancel
claims 41-47 without prejudice. Please add new claims 55-61.

- 1 33. (Amended) A method of multiplexing signals onto an
2 interconnect line comprising:
3 enabling a first tristate driver having an input coupled to a first logic
4 element and an output directly connected to the interconnect line, such that a first signal